

TITLE

NON-INVERTING DOMINO REGISTER

by

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CROSS-REFERENCE TO RELATED APPLICATIONS

Sc [0001] This application claims the benefit of U.S. Provisional Application No. 60/402962, filed on 8/13/02, which is herein incorporated by reference for all intents and purposes. ¹⁴₁

BACKGROUND OF THE INVENTION

FIELD OF THE INVENTION

[0002] The present invention relates to dynamic logic and register functions, and more particularly to a non-inverting domino register that addresses the problem of registering the outputs of complex logic circuits where speed and size are important factors.

DESCRIPTION OF THE RELATED ART

[0003] Integrated circuits use a remarkable number of registers, particularly those having a synchronous pipeline architecture. Register logic is employed to hold the outputs of devices and circuits for a period of time so that these outputs can be received by other devices and circuits. In a clocked system, such as a pipeline